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A. Neamen Exercise Solutions ____ EX1.8 ?V ? VPS = I D R + VD and I D ? I S exp ? D ? ? VT ? (4 ? VD) so 4 = I D (4 × 103) + VD ? I D = 4 ×103 and ? V ? I D = (10 ?12) exp ? D ? ? 0.026 ? By trial and error, we find I D ?

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Assume that the BJT in the common-emitter circuit shown in Figure 8.4 has limiting factors of: , and .Neglecting second breakdown effects, determine the minimum value of R L such that the Q-point of the transistor always stays within the safe operating area for: (a) , and (b) .In each case, determine the maximum collector current and maximum transistor power dissipation.

Chapter 8 Solutions | Microelectronics Circuit Analysis ...
Donald A. Neamen Microelectronics: Circuit Analysis and Design is intended as a core text in electronics for undergraduate electrical and computer engineering students. The fourth edition continues to provide a foundation for analyzing and designing both analog and digital electronic circuits.

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